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परीक्षा विभाग



Dr. Narendra S. Jadhav

Controller of Examination(I/c)

Ref. No. DBATU/Suppl/EXAM/M. Tech S-25/TT/ 2025/5584

डॉ. नरेंद्र सु. जाधव

परीक्षा नियंत्रक (प्र.)

Date: 06-09-2025

M.Tech Time Table Supplementary Summer - 2025
VLSI & Allied Course- (12341/12968)

Sr.No	Date	Time	Course	Subject Code	Subject Name
1	16/09/2025	02.00 PM to 05.00 PM	M. Tech (VLSI & Embedded Systems)	24AF2341PC101	VLSI Technology and Design
				MTVEC101	VLSI Technology and Design
			M.Tech Electronics and Communication (VLSI Design)	24AF2968PC101	RTL Simulation & Synthesis with PLDs
2	17/09/2025	02.00 PM to 05.00 PM	M. Tech (VLSI & Embedded Systems)	24AF2341PC102	Advanced Embedded Logic
			MTVEC102	CMOS Analog Circuit Design	
			M.Tech Electronics and Communication (VLSI Design)	24AF2968PC102	Advanced Digital Signal Processing
			3	18/09/2025	02.00 PM to 05.00 PM
24AF2341PE103B	Digital System Design				
24AF2341PE103C	Soft Computing Techniques				
24AF2341PE103D	CPLD and FPGA Architectures and Applications				
24AF2341PE103E	Advanced Computer Architecture				
MTVEC103	Advanced Embedded Logic				
M.Tech Electronics and Communication (VLSI Design)	24AF2968PE103A	Image Processing & Computer Vision			
	24AF2968PE103B	Programming Language for Embedded Systems			
	24AF2968PE103C	Mixed Signal Design			
	24AF2968PE103D	RF Engineering			
	24AF2968PE103E	VLSI Signal Processing			
4	19/09/2025	02.00 PM to 05.00 PM	M. Tech (VLSI & Embedded Systems)	24AF2341PE104A	Advanced Operating Systems
				24AF2341PE104B	Network Security and Cryptography
				24AF2341PE104C	CMOS Digital Integrated Circuit Design
				24AF2341PE104D	Embedded C
				24AF2341PE104E	Optical Communication Design & Test
				MTVEE114A	Hardware Software Co-Design
				MTVEE114B	Digital System Design
				MTVEE114C	Soft Computing Techniques
				MTVEE114D	CPLD and FPGA Architectures and Applications
				MTVEE114E	Advanced Computer Architecture
			M.Tech Electronics and Communication (VLSI Design)	24AF2968PE104A	Embedded System Design
				24AF2968PE104B	Parallel Processing
				24AF2968PE104C	System Design with Embedded Linux
				24AF2968PE104D	CAD of Digital Systems
24AF2968PE104E	ASIC Design				
5	20/09/2025	02.00 PM to 05.00 PM	M.Tech Electronics and Communication (VLSI Design)	24AF2968RM105	Research Methodology
			M. Tech (VLSI & Embedded Systems)	MTVEE125A	Advanced Operating Systems
				MTVEE125B	Network Security and Cryptography
				MTVEE125C	CMOS Digital Integrated Circuit Design
				MTVEE125D	Embedded C
				MTVEE125E	Optical Communication Design & Test

Important Note: 1.The candidates appearing for the examination should report 20 minutes before the start of examination.
2. Mobile phones and other electronic gadgets are prohibited in the examination hall.



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